# California Instruments Ls Series

### 3-18 kVA Programmable AC Power Source / Analyzer

- Backward Compatible with L Series
   Function and bus compatible with the California
   Instruments L Series
- Three phase and Single phase modes Ideally suited for avionics and defense applications
- 3 kVA to 18 kVA Power Levels Match power source and cost to application requirements
- Transient Programming Test products for susceptibility to AC line disturbances
- Built-in Measurements Performs voltage, current, and power measurements
- Advanced Features Arbitrary waveform generation, harmonic analysis, GPIB interface are some of the available options
- Interface Standard USB & RS232C interface. Optional GPIB & LAN available
- CE Marked (400V Input model ONLY) Safe, reliable, and consistent operation

#### Integrated System

The Ls Series is an improved version of the classic California Instruments L Series AC power sources. The Ls Series provides many basic AC source capabilities at an economical cost. Additional capabilities such as arbitrary waveform generation and harmonic analysis can be added as options.

The Ls Series can be ordered in either single phase (-1) or three phase (-3) configurations. Power levels range from 3 kVA to 6 kVA in a single chassis. Multiple chassis can be combined for power levels up to 18 kVA.

#### Easy-To-Use Controls

The Ls Series is completely microprocessor controlled and can be operated from simple front panel controls. A pair of analog controls located next to the backlit alphanumeric LCD display allows output voltage and frequency to be slewed up or down dynamically. For more advanced operations, a series of menus is provided using a dual line high contrast LCD display. An optional full keypad is available.





		0–13	82 A
$\approx$	208	230	400
$\sim$		230	
ETHERNE	<u>Us</u> e		RS232

135–400 V

#### Applications

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> With precise output regulation and accuracy, high load drive current, multi or single phase mode and built-in measurement capabilities, Ls Series AC sources address many application areas of AC power testing. Additional features such as DO 160, MIL 704, Boeing, or Airbus test standards are available options that establishes the Ls Series as a solid choice for avionics or defense applications. All Ls Series AC sources are standard equipped with USB and RS232C remote control interfaces. GPIB and Ethernet (LAN) interfaces are optional.

#### Compatibility

Although the standard command language is SCPI, the Ls Series also offers functional and bus compatibility with the CI L Series AC power sources. Using the APE (Abbreviated Plain English) command syntax, the Ls Series can be used in existing test systems without having to modify program code. The APE language is part of the -GPIB option which includes a GPIB/ IEEE-488 interface.

#### AMETEK Programmable Power 9250 Brown Deer Road San Diego, CA 92121-2267 USA



### 3000–18000 VA

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Ls Series

#### **Transient Programming**

To simulate common line disturbance occurrences, the Ls Series offers a list of transient steps. These steps can be programmed from the front panel or downloaded over the interface using the Interface Instrument Control Software (GUI) program supplied. The GUI allows libraries of commonly used line disturbances to be created on disk for guick recall. Once downloaded, the transient program can be executed from the PC or from the front panel. AC transient generation allows the effect of rapid changes in voltage, frequency, phase angle and waveform shape on the unit under test to be analyzed. The Ls Series is available in either three or one phase output configurations and offers standard voltage ranges of 135 Vrms and 270 Vrms. A wide range of options can be added to customize the Ls Series to meet your specific application requirements.

#### Voltage Range Options

Output voltage range options are available to provide higher voltage outputs. In addition to the standard 135/270 V range pair, 156/312 Vrms (-HV option) or 200/400 Vrms (-EHV option) can be specified at the time of order. All voltage ranges are Line to Neutral. On three phase Ls Series models, maximum Line to Line voltages are 467 V (standard), 540 V (-HV option) and 692 V (-EHV option).

#### Phase Mode

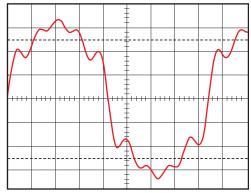
The -MODE option provides automatic switching between three phase and single phase output modes. In single phase mode, all output current is routed to the Phase A output terminal. The -MODE option is available for 3 phase Ls configurations.

#### Waveform Generation

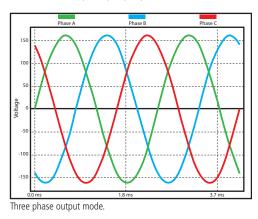
The standard Ls Series provides sine wave output capability. For more demanding test applications, the advanced option package (-ADV) adds the following waveform capabilities:

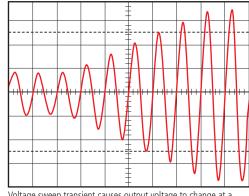
- Squarewave.
- Clipped Sinewave Simulates THD levels to test for harmonic distortion susceptibility.
- Harmonic and Arbitrary (User defined) waveforms.

Using the provided Windows GUI, defining harmonic waveforms is as easy as specifying the relative amplitude and phase angle for each of up to the 50th harmonic. The waveform data points are generated and downloaded by the ICS to the AC source through the standard RS232C, USB or optional LAN or GPIB bus and are retained in non-volatile memory. Up to 50 waveforms can be stored and named for easy recall.









Voltage sweep transient causes output voltage to change at a programmed rate.

#### Ls Series - Measurement and Analysis

The Ls Series measurement system is based on real-time digitization of the voltage and current waveforms using a 4K sample buffer. The digitized waveform data is processed by a Digital Signal Processor to extract conventional load values such as rms voltage, rms current, real and apparent power. With the addition of the advanced features option. (-ADV option), the same data can also be used to perform Fast Fourrier Transformation (FFT) to extract the harmonic amplitude and phase angle of 50 harmonics, or display acquired voltage and current waveforms. ronics Please visit us at: www.valuetronics.com

### Ls Series

#### **Standard Measurements**

The following standard measurements are available from the front panel or via the bus:

- Frequency and Phase
- Voltage (rms)
- Current(rms) and Peak Current
- Crest Factor
- Real Power and Apparent Power
- Power Factor

#### Advanced Measurement Functions (-ADV option)

Power analysis of EUT load characteristics is available by adding the -ADV option. Harmonics up to the 50th harmonic (for fundamental frequencies up to 250 Hz) and total harmonic distortion of both voltage and current is provided as well.

Harmonic analysis data can be displayed on the front panel display or on the PC using the GUI program. The GUI can also be used to save and print harmonics data in tabular, bar graph or time domain formats.

The acquired voltage and current time-domain waveforms for each output phase can be displayed using the GUI program. Waveform displays on the PC. Available display modes include voltage and current combined, three phase voltage, three phase current and true power. The time-domain data is also available for transfer to a PC through the bus when using custom software.

#### **Diagnostics Capability**

The AC Source can perform a self test and report any errors. The self test will run until the first error is encountered and terminate. The response to the self test query command will either be the first error encountered or 0 if no error was found. (Self test passed).

#### Windows Graphical User Interface

A Windows compatible Instrument Control Software (GUI) offers a soft front panel interface for operation from a PC. The following functions are available through this GUI program:

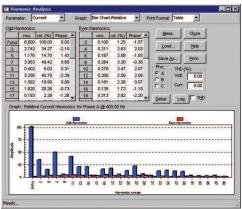
- Steady state output control (all parameters).
- Create, run, save and print transient programs.
- Measure and log standard measurements.

#### With -ADV option:

- Generate and save harmonic waveforms.
- Generate and save arbitrary waveforms.
- Capture and display Voltage and Current waveforms.
- Measure, display, print and log harmonic voltage and current measurements.



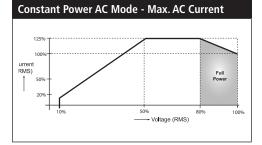
Standard measurements for all phases.



Standard measurements for all phases.

Frequency:		5000	1000.0	Output Relay:	Cosed
		Auto Level Cr	ontrol:	Voltage Range: • 135 V	270 V
Ampl (Y)	0.0	I         PhsA         I         PhsB           0.0         0.0         0.0           5.00         5.00         5.00	0.0	Overload Model CC 0.1 CV	
2hase (*)		<b>○</b> 0.0 ○ 240.0	120.0	Sense Lines:	Extern.

Standard measurements for all phases.



## 3000–18000 VA

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# Ls Series : Specifications

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Output												
Maximum Power per phase	3000Ls: 1 phase: 3000 VA, 3 phase: 1000 VA; 4500Ls: 1 phase 4500 VA, 3 phase 1500 VA; 6000Ls: 1 phase 6000 VA, 3 phase: 2000 VA											
Power factor	0 to unity	0 to unity at full output VA										
Voltage Ranges	Range	V Lo	w V	High	VA Program	ming Resolutio	n 10	10 mV				
	AC	0-13	35V 0-	-270V	Load Regula	ation	<	0.1 % FS				
		Line Regulation < 0.02 % for 10 % line change										
	See -HV a	See -HV and EHV options for alternative voltage range pairs.										
Programming Accuracy (25°C ±5°C		Voltage (rms): $\pm$ (0.05% + 0.25) V from 5.0 V to FS; Frequency: $\pm$ 0.025 45 Hz - 819.1 Hz, $\pm$ 0.7 % > 819.1 Hz; Phase: $\pm$ 1° 45-100 H $\pm$ (1° + 1°/kHz) 100 Hz-1kHz										
Frequency Range	45 Hz - 10	45 Hz - 1000 Hz (see -HF option for higher output frequencies) 17 - 45 Hz operation available at reduced voltages										
Frequency Resolution	0.01 Hz at	< 81.9	Hz, 0.1	Hz at 82.0 to	o 819.1 Hz, 1 H	lz2 at > 819 Hz	2					
Max RMS Current	V Range	V high	V low	At Full Pov	wer Model	3000Ls-3 Ø	3000  s-1 Ø	4500l s-3	Ø 45001s-10	Ø 6000Ls-3 Ø	6000Ls-1 Ø	
		7.4 A	14.8 A	At FS Voltag		+ +	22.2 A	11.1 A	33.3 A	14.8 A	44.4 A	
	-1 1ø	22.2 A	44.4 A		V High		11.1 A	5.5 A	16.7 A	7.4 A	22.2 A	
				1 3000Ls and 450	-	ased current at redu	1		1	1		
Comment Linet	D							· · · ·		-		
Current Limit					m current for s	5						
Peak Current				5		rms @ full scale	5					
Output Noise	100mV rm	s typ. (2	20 kHz to	o 1 MHz)	Harmonic Di	stortion < 1	% (at full sc	ale voltage	, full resistive	load)		
Isolation Voltage	300 V rms	output	to chass	is	Output Relay	Pus	h button cor	trolled and	d bus controlle	ed output relay		
Input												
Voltage	Models 30	00Ls, 4	500Ls, 9	000Ls, 13500	JLS: Standard:	200 200 1 107			· · · · ·			
	Models 60	00Ls, 1.	2000Ls, e specified	18000Ls: Sta	ndard 208-230	) + 10% VAC (I t availble on 6000L 6000Ls (@ 2	L-L, 3 Phase) s, 12000Ls, 180 08V) In	450V L- 00Ls. 3. 3000 rush Curre	DLs can be operat	ed from 1 phase A 80-254 V: 50 A	peak	
Voltage	Models 60 Notes: 1. Inpr Model 187 VLL	00Ls, 12 ut must bu 300 19	2000Ls, e specified 0Ls 3 A	18000Ls: Sta	ndard 208-230 2400 option no se) 4500Ls 31 A	) + 10% VAC (L t availble on 6000L	L, 3 Phase) s, 12000Ls, 180 08V) In (F	450V L- 00Ls. 3. 3000 rush Curre er phase):	DLs can be operat nt @ 18 @ 36	ted from 1 phase A 80-254 V: 50 A 50-440 V: 83 A	peak	
Voltage	Models 60 Notes: 1. Inpr Model	00Ls, 12 ut must b 300	2000Ls, e specified 0Ls 3 A	18000Ls: Sta when ordering. 000Ls (1Pha:	ndard 208-230 2400 option no se) 4500Ls	) + 10% VAC (L t availble on 6000L 6000Ls (@ 2	L, 3 Phase) s, 12000Ls, 180 08V) In (F	450V L- 00Ls. 3. 3000 rush Curre	DLs can be operat nt @ 18 @ 36	ed from 1 phase A 80-254 V: 50 A	peak	
Voltage	Models 60 Notes: 1. Inpr Model 187 VLL	00Ls, 12 ut must bi 300 19 10	2000Ls, e specified 0Ls 3 A	18000Ls: Sta when ordering. 000Ls (1Pha: 32 A	ndard 208-230 2400 option no se) 4500Ls 31 A	) + 10% VAC (L t availble on 6000L 6000Ls (@ 2 38 A	L, 3 Phase) s, 12000Ls, 180 08V) In (F	450V L- 00Ls. 3. 3000 rush Curre er phase):	DLs can be operat nt @ 18 @ 36	ted from 1 phase A 80-254 V: 50 A 50-440 V: 83 A	peak	
Voltage Line Current (rms per phase)	Models 60 Notes: 1. Input Model 187 VLL 360 VLL	00Ls, 12 ut must bi 300 19 10	2000Ls, e specified 0Ls 3 A	18000Ls: Sta when ordering. 000Ls (1Pha: 32 A	ndard 208-230 2400 option no se) 4500Ls 31 A	) + 10% VAC (L t availble on 6000L 6000Ls (@ 2 38 A	L, 3 Phase) s, 12000Ls, 180 08V) In (F	450V L- 00Ls. 3. 3000 rush Curre er phase):	DLs can be operat nt @ 18 @ 36	ted from 1 phase A 80-254 V: 50 A 50-440 V: 83 A	peak	
Voltage Line Current (rms per phase) Efficiency	Models 60 Notes: 1. Inpu Model <u>187 VLL</u> 360 VLL 75% typica	00Ls, 1. ut must bu 300 19 10	2000Ls, e specified 0Ls 3 A	18000Ls: Sta when ordering. 000Ls (1Pha: 32 A	ndard 208-230 2400 option no se) 4500Ls 31 A	) + 10% VAC (L t availble on 6000L 6000Ls (@ 2 38 A	L, 3 Phase) s, 12000Ls, 180 08V) In (F	450V L- 00Ls. 3. 3000 rush Curre er phase):	DLs can be operat nt @ 18 @ 36	ted from 1 phase A 80-254 V: 50 A 50-440 V: 83 A	peak	
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Voltage Line Current (rms per phase) Efficiency Power Factor Hold-up Time System	Models 60           Notes: 1. Input           Model           187 VLL           360 VLL           75% typica           0.6 typical           At least 10	00Ls, 1: ut must b 300 19 10 10 10 ms	2000Ls, e specified OLs 3 A A A	18000Ls: Sta when ordering. 000Ls (1Pha: 32 A n/a	ndard 208-23( 2400 option no se) 4500Ls 31 A 16 A	) + 10% VAC (L t availble on 6000L 6000Ls (@ 2 38 A n/a	L, 3 Phase) s, 12000Ls, 180 08V) In (F Li	450V L- 00Ls. 3. 3000 rush Curre er phase): ne Frequen	DLs can be operat	eed from 1 phase A 30-254 V: 50 A 50-440 V: 83 A 40 Hz	peak peak	
Voltage Line Current (rms per phase) Efficiency Power Factor Hold-up Time System Storage	Models 60           Notes: 1. Input           Model           187 VLL           360 VLL           75% typical           0.6 typical           At least 10           Setup: 16 0	00Ls, 1: ut must bi 300 19 10 10 0 ms complet	2000Ls, e specified OLs 3 A A A =	18000Ls: Sta when ordering. 000Ls (1Pha: 32 A n/a ment setups	ndard 208-23( 2400 option no se) 4500Ls 31 A 16 A / Transient Lis	) + 10% VAC (L t availble on 6000L 6000Ls (@ 2 38 A n/a : 100 transient	L, 3 Phase) s, 12000Ls, 180 08V) In (F Li steps per list	450V L- 00Ls. 3. 3000 rush Curre (er phase): ne Frequen	DLS can be operat nt @ 18 @ 36 ncy: 47-4 de) or 16 trans	eed from 1 phase A 30-254 V: 50 A 50-440 V: 83 A 40 Hz sient registers (	peak peak	
Voltage Line Current (rms per phase) Efficiency Power Factor Hold-up Time <b>System</b> Storage Trigger Input/Output	Models 60           Notes: 1. Input           Model           187 VLL           360 VLL           75% typical           0.6 typical           At least 10           Setup: 16 0	00Ls, 1: ut must bi 300 19 10 10 0 ms complet	2000Ls, e specified OLs 3 A A A =	18000Ls: Sta when ordering. 000Ls (1Pha: 32 A n/a ment setups	ndard 208-23( 2400 option no se) 4500Ls 31 A 16 A / Transient Lis	) + 10% VAC (L t availble on 6000L 6000Ls (@ 2 38 A n/a	L, 3 Phase) s, 12000Ls, 180 08V) In (F Li steps per list	450V L- 00Ls. 3. 3000 rush Curre (er phase): ne Frequen	DLS can be operat nt @ 18 @ 36 ncy: 47-4 de) or 16 trans	eed from 1 phase A 30-254 V: 50 A 50-440 V: 83 A 40 Hz sient registers (	peak peak	
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Voltage Line Current (rms per phase) Efficiency Power Factor Hold-up Time System Storage Trigger Input/Output Protection Overload/Temp/Voltage	Models 60 Notes: 1. Input Model 187 VLL 360 VLL 75% typical 0.6 typical At least 10 Setup: 16 0 Input: Trigg	00Ls, 1: at must b 300 19 10 10 10 10 10 10 10 10 10 10	2000Ls, e specified OLS 3 A A A e instrur asureme nt curren	18000Ls: Sta when ordering. 000Ls (1Pha: 32 A n/a ment setups nts or transie t or constant	ndard 208-23( 2400 option no se) 4500Ls 31 A 16 A / Transient Liss nt steps - SMA voltage mode,	0 + 10% VAC (L t availble on 6000L 6000Ls (@ 2 38 A n/a : 100 transient connector: 10k Over temperati	L, 3 Phase) s, 12000Ls, 180 08V) In (F Li steps per list ( pull-up /	450V L- 00Ls. 3. 3000 rush Curre er phase): ne Frequen : (SCPI moo Output	DLS can be operat nt @ 18 @ 36 ncy: 47-4 de) or 16 tran: : SMA Connec	ed from 1 phase A 30-254 V: 50 A 50-440 V: 83 A 40 Hz sient registers ( ctor: HCTTL out	peak peak APE mode) put	
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Voltage Line Current (rms per phase) Efficiency Power Factor Hold-up Time System Storage Trigger Input/Output Protection Overload/Temp/Voltage Regulatory/RFI Suppresion	Models 60           Notes: 1. Input           Model           187 VLL           360 VLL           75% typical           At least 10           Setup: 16 0           Input: Trigg           Overload: 1           IEC1010, E	00Ls, 1: at must b 3000 19 10 10 10 10 10 10 10 10 10 10	2000Ls, e specified OLS 3 A A A e instrur asureme at curren 1-2, ENS	18000Ls: Sta when ordering. 000Ls (1Pha: 32 A n/a ment setups nts or transie t or constant 50082-2, CE	ndard 208-23( 2400 option no se) 4500Ls 31 A 16 A / Transient Lis nt steps - SMA voltage mode, (for 400V inpu	0 + 10% VAC (L t availble on 6000L 6000Ls (@ 2 38 A n/a : 100 transient connector: 10k Over temperati	L, 3 Phase) s, 12000Ls, 180 08V) In (F Li steps per list ( pull-up / ure: Automat	450V L- 00Ls. 3. 3000 rush Curre er phase): ne Frequen : (SCPI moo Output ic Shutdow	DLS can be operat nt @ 18 @ 36 ncy: 47-4 de) or 16 tran: : SMA Connec	ed from 1 phase A 30-254 V: 50 A 50-440 V: 83 A 40 Hz sient registers ( ctor: HCTTL out	peak peak APE mode) put	
Voltage Line Current (rms per phase) Efficiency Power Factor Hold-up Time System Storage Trigger Input/Output Protection Overload/Temp/Voltage Regulatory/RFI Suppresion Measurement	Models 60 Notes: 1. Input 360 VLL 75% typical 0.6 typical At least 10 Setup: 16 0 Input: Trigg Overload: 1 IEC 1010, E EMC, and 3	00Ls, 1: ut must b 3000 300 19 10 10 10 10 10 10 10 10 10 10	2000Ls, e specified OLS 3 A A A ete instrur asureme nt curren 1-2, ENE nark requ	18000Ls: Sta when ordering. 000Ls (1Pha: 32 A n/a ment setups nts or transie t or constant 50082-2, CE uirements /	ndard 208-23( 2400 option no se) 4500Ls 31 A 16 A / Transient List nt steps - SMA voltage mode, (for 400V inpu RIF Suppressi	) + 10% VAC (L availble on 6000L 6000Ls (@ 2 38 A n/a : 100 transient connector: 10P Over temperatu t only), on: CISPR 11, G	L, 3 Phase) s, 12000Ls, 180 08V) In (F Li steps per list ( pull-up / ure: Automat iroup1, Class	450V L- 00Ls. 3. 3000 rush Curre er phase): ne Frequen : (SCPI moo Output ic Shutdow	DLS can be operat nt @ 18 @ 36 icy: 47-4 de) or 16 tran: : SMA Connect vn; Over volta	ed from 1 phase A 30-254 V: 50 A 50-440 V: 83 A 40 Hz sient registers ( ctor: HCTTL out ge: Automatic :	peak peak APE mode) put shutdown	
Voltage Line Current (rms per phase) Efficiency Power Factor Hold-up Time System Storage Trigger Input/Output Protection Overload/Temp/Voltage Regulatory/RFI Suppresion Measurement Measurements - Standard	Models 60           Notes: 1. Input           Model           187 VLL           360 VLL           75% typical           At least 10           Setup: 16 0           Input: Trigg           Overload: 1           IEC1010, E	00Ls, 1: ut must b 3000 300 19 10 10 10 10 10 10 10 10 10 10	2000Ls, e specified OLS 3 A A A e instrur asureme at curren 1-2, ENS	18000Ls: Sta when ordering. 000Ls (1Pha: 32 A n/a ment setups nts or transie t or constant 50082-2, CE uirements /	ndard 208-23( 2400 option no se) 4500Ls 31 A 16 A / Transient Lis nt steps - SMA voltage mode, (for 400V inpu	0 + 10% VAC (L t availble on 6000L 6000Ls (@ 2 38 A n/a : 100 transient connector: 10P Over temperatu t only),	L, 3 Phase) s, 12000Ls, 180 08V) In (F Li steps per list ( pull-up / ure: Automat	450V L- 00Ls. 3. 3000 rush Curre er phase): ne Frequen : (SCPI moo Output ic Shutdow	DLS can be operat nt @ 18 @ 36 ncy: 47-4 de) or 16 tran: : SMA Connec	eed from 1 phase A 30-254 V: 50 A 50-440 V: 83 A 40 Hz sient registers ( ctor: HCTTL out ge: Automatic :	peak peak APE mode) put shutdown	
Voltage Line Current (rms per phase) Efficiency Power Factor Hold-up Time System Storage Trigger Input/Output Protection Overload/Temp/Voltage Regulatory/RFI Suppresion Measurement	Models 60 Notes: 1. Input 360 VLL 75% typical 0.6 typical At least 10 Setup: 16 0 Input: Trigg Overload: 1 IEC 1010, E EMC, and 3	00Ls, 1: at must b 300 19 10 10 10 10 10 10 10 10 10 10	2000Ls, e specified OLS 3 A A A e instrum asureme nt curren 1-2, ENS nark required requency 15-81.91 :20-819.	18000Ls: Sta when ordering. 000Ls (1Pha: 32 A n/a ment setups nts or transie t or constant 50082-2, CE uirements / Hz	ndard 208-23( 2400 option no se) 4500Ls 31 A 16 A / Transient List nt steps - SMA voltage mode, (for 400V inpu RIF Suppressi	) + 10% VAC (L availble on 6000L 6000Ls (@ 2 38 A n/a : 100 transient connector: 10P Over temperatu t only), on: CISPR 11, G	L, 3 Phase) s, 12000Ls, 180 08V) In (F Li steps per list ( pull-up / ure: Automat iroup1, Class	450V L- 00Ls. 3. 3000 rush Curre (ar phase): ne Frequen (SCPI moo Output (c Shutdow A AC rms) F	DLS can be operat nt @ 18 @ 36 icy: 47-4 de) or 16 tran: : SMA Connect vn; Over volta	ed from 1 phase A 30-254 V: 50 A 50-440 V: 83 A 40 Hz sient registers ( ctor: HCTTL out ge: Automatic :	peak peak APE mode) put shutdown	
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Voltage Line Current (rms per phase) Efficiency Power Factor Hold-up Time System Storage Trigger Input/Output Protection Overload/Temp/Voltage Regulatory/RFI Suppresion Measurement Measurements - Standard	Models 60 Notes: 1. Input 360 VLL 75% typical 0.6 typical At least 10 Setup: 16 0 Input: Trigg Overload: 1 IEC 1010, E EMC, and 3 Parameter Range	00Ls, 1: at must b 300 300 19 10 10 10 10 10 10 10 10 10 10	2000Ls, e specified OLS 3 A A A e instrum asureme nt curren 1-2, ENS nark required requency 15-81.91 :20-819.	18000Ls: Sta when ordering. 000Ls (1Pha: 32 A n/a ment setups nts or transie t or constant 50082-2, CE uirements / Hz 1 Hz	ndard 208-230 2400 option no se) 4500Ls 31 A 16 A / Transient Lis / Transient Lis nt steps - SMA voltage mode, (for 400V inpu RIF Suppressi Phase 45-100 Hz 100-1000 Hz 0.5°	) + 10% VAC (L t availble on 6000L 6000Ls (@ 2 38 A n/a         	L, 3 Phase) s, 12000Ls, 180 08V) In (F Li steps per lisi (pull-up / ure: Automat roup1, Class Current ( 0-50 A V 0.1% +	450V L- 00Ls. 3. 3000 rush Curre ter phase): ne Frequen (SCPI moo Output cic Shutdow AC rms) F (C 150 mA C	DLS can be operat nt @ 18 @ 36 icy: 47-4 de) or 16 tran: : SMA Connect vn; Over volta Real Power D-6 kW D.15% + 9 W	ed from 1 phase A 30-254 V: 50 A 50-440 V: 83 A 40 Hz sient registers ( ctor: HCTTL out ge: Automatic : ge: Automatic : Apparent Power 0-6 kVA 0.15% + 9 VA	peak peak APE mode) put shutdown	
Voltage Line Current (rms per phase) Efficiency Power Factor Hold-up Time System Storage Trigger Input/Output Protection Overload/Temp/Voltage Regulatory/RFI Suppresion Measurement Measurements - Standard	Models 60 Notes: 1. Input 360 VLL 75% typical 0.6 typical At least 10 Setup: 16 0 Input: Trigg Overload: 4 IEC 1010, E EMC, and 3 Parameter Range Accuracy* 1 1 ø mode ( 3 ø mode (	00Ls, 1: it must b 3000 3000 19 10 10 10 10 10 10 10 10 10 10	2000Ls, e specified 0Ls 3 A A a a a a a a a a a a a a a	18000Ls: Sta when ordering. 000Ls (1Pha: 32 A n/a ment setups nts or transie t or constant 50082-2, CE uirements / Hz 1 Hz digit	ndard 208-230 2400 option no se) 4500Ls 31 A 16 A / Transient Lis' voltage mode, (for 400V inpu RIF Suppressi Phase 45-100 Hz 100-1000 Hz 0.5° 2°	0 + 10% VAC (L availble on 6000L 6000Ls (@ 2 38 A n/a : 100 transient : 100 transient connector: 10P Over temperation t only), on: CISPR 11, G Voltage (AC) 0-400 V 0.5% + 250 m	L, 3 Phase) s, 12000Ls, 180 08V) In (F Li steps per lisi (pull-up / ure: Automat roup1, Class Current ( 0-50 A V 0.1% + 0.1% +	450V L- 00Ls. 3. 3000 rush Curre ter phase): ne Frequen (SCPI moo Output c (SCPI moo Output AC rms) F 150 mA C 50 mA C	DLS can be operat nt @ 18 @ 36 icy: 47-4 de) or 16 tran: : SMA Connect vn; Over volta Real Power D-6 kW D.15% + 9 W D.15% + 3 W	ed from 1 phase A 30-254 V: 50 A 50-440 V: 83 A 40 Hz sient registers ( ctor: HCTTL out ge: Automatic : ge: Automatic : 0-6 kVA 0.15% + 9 VA 0.15% + 3 VA	peak peak APE mode) put shutdown Power Factor 0.00-1.00 0.03 0.01	
Voltage Line Current (rms per phase) Efficiency Power Factor Hold-up Time System Storage Trigger Input/Output Protection Overload/Temp/Voltage Regulatory/RFI Suppresion Measurement Measurements - Standard	Models 60 Notes: 1. Inp Model 187 VLL 360 VLL 75% typica 0.6 typical At least 10 Setup: 16 0 Input: Trigg Overload: 1 IEC 1010, E EMC, and 3 Parameter Range Accuracy* 1 ø mode ( 3 ø mode ( Resolution)	00Ls, 1: at must bi 3000 19 10 10 10 10 10 10 10 10 10 10	2000Ls, e specified OLS 3 A A A A a te instrur asureme at curren 1-2, ENS nark requ requency 5-81.91 2.0-819. - 819 Hz 0.1% + 1 01 Hz / 0	18000Ls: Sta when ordering. 000Ls (1Pha: 32 A n/a ment setups nts or transie t or constant 50082-2, CE uirements / Hz 1 Hz digit .1 Hz / 1 Hz	ndard 208-230 2400 option no se) 4500Ls 31 A 16 A / Transient Lis' voltage mode, (for 400V inpu RIF Suppressi Phase 45-100 Hz 100-1000 Hz 0.5° 2° 0.1° / 1°	0 + 10% VAC (L t available on 6000L 6000Ls (@ 2 38 A n/a : 100 transient : 100 transient : 100 transient : 100 transient : 0ver temperation t only), on: CISPR 11, G Voltage (AC) 0-400 V	-L, 3 Phase) s, 12000Ls, 180 08V) In (F Li steps per list ( pull-up / ure: Automat roup1, Class Current ( 0-50 A V 0.1% + 0.1% + 1 mA	450V L- 00Ls. 3. 3000 rush Curre (rush Curre (rush Curre (s CPI moo 0 utput (s Shutdov A AC rms) F ( 150 mA C 50 mA C 1 1 1 1 1 1 1 1 1 1 1 1 1	DLS can be operat nt @ 18 @ 36 icy: 47-4 de) or 16 tran: : SMA Connect vn; Over volta Real Power D-6 kW D.15% + 9 W D.15% + 3 W I W	ed from 1 phase A 30-254 V: 50 A 50-440 V: 83 A 40 Hz sient registers ( ctor: HCTTL out ge: Automatic : ge: Automatic : 0-6 kVA 0.15% + 9 VA 0.15% + 9 VA 1 VA	peak peak peak APE mode) put shutdown shutdown Power Factor 0.00-1.00 0.03 0.01 0.01	

Note: Specifications are subject to change without notice. Specifications are warranted over an ambient temperature range of 25°± 5° C. Unless otherwise noted, specifications are per phase for a sinewave with a resistive load and apply after a 30 minute warm-up period. For three phase configurations, all specifications are for L-N. Phase angle specifications are valid under balanced load conditions only.

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# Ls Series : Specifications

3000-18000 VA

Remote Control										
IEEE-488 Interface (option)	IEEE-488 (GPI	B) talker listener. Subse	t: AH1, C0, [	DC1, DT1, L3, PP0, RL2, SH1,	, SR1, T6, IEEE-48	8.2 SCPI Synt	ах			
USB Interface & Ethernet	Version: USB 1	Version: USB 1.1; Speed: 460 Kb/s maximum / Ethernet Interface (Optional): specify -LAN option. 10BaseT, 100BaseT, RJ45								
RS232C Interface		Bi-directional serial interface; 9-pin D-shell connector. Handshake: CTS, RTS. Databits: 7 w/ parity, 8 w/o parity. Stopbits: 2. Baud rate: 9600 to 115200. Supplied with RS232C cable / Code and Format: SCPI; APE (option -GPIB)								
Physical Dimensions										
Dimensions (per chassis)	Height: 10.5"	(267 mm), Width: 19"	(483 mm), C	0epth: 23.7" (602 mm) (dept	th includes rear p	anel connecto	rs)			
Weight	Chassis: Net: 1	93 lbs / 87.7 Kg, Shipp	oing: 280 lbs	; / 127.3 Kg (for /2 or /3 mod	del configuaratior	ns multiply nu	mber of chassis	5)		
Vibration and Shock	Designed to m	eet NSTA project 1A tra	ansportatior	ı levels						
Air Intake/Exhaust	Forced air coo	ing, side air intake, rea	r exhaust							
Temperature & Diagnostics	Temperature: (	Dperating: 0 to 35° C, f	ull power / S	Storage: -40 to +85° C; Dia	gnostics: Built-in	self test avail	able over bus (	*TST)		
Rear Panel Connectors	connector (RS	232 DB9 to DB9 cable s	supplied). *	with safety cover. * IEEE-48 Remote Inhibit (INH) and Di terface connectors. * Auxilar	iscrete Fault Indic	ator (DFI). * I	PIB). * 9-pin D Remote voltage	-Shell RS232C e sense terminal		
<b>Option -AX Specifications</b>										
Option -AX	the 5 V for lan	np power. 26 Volt-Accu	$racy: \pm 2\%$ .	5 Vac unregulated outputs. 1 Current capacity: 3 ARMS. F y: $\pm$ 5%. Current capacity: 5	requency:	Ily used for se	ervo-synchro ex	citation, and		
<b>Option -ADV Specifications</b>										
Measurements - Harmonics	Parameter	Frequency Fundame	ntal Harmon	ics Voltage		Current				
	Range	45-250 Hz / 0.09 -	12.5 kHz	Fundamental Harmonic	cs 2 - 50	Fundamenta	I Harmonics 2	- 50		
	Accuracy* (±)	Accuracy* (±) 0.01% + 1 digit / 0.5% + 1 digit			nV+0.3% /1 kHz			0.3% /1 kHz		
	Resolution * Accuracy specif	0.01 Hz / 0.1 Hz	ading for singl	10 mV / 10 mV e unit in 3-phase mode.		10 mA / 10	mA	.A		
Waveforms	Pre defined: Si	ne, Square, Clipped Use	er defined, 1	024 addressable data points	s; Storage: 50 use	r waveforms,	non-volatile m	emory		
Data Acquisition	Parameters: Vo	ltage, Current time dor	nain, per ph	ase; Resolution: 4096 data p	points, 10.4 usec	(1ø) or 31.25	usec (3ø) sam	pling interval		
Option -HV Specifications										
Voltage/Frequency Ranges		lt; High: 0-312 Volt / Fr 5 Hz - 5000 Hz	equency: W	ith -HF option: 3000Ls, 4500	)Ls, 6000Ls: 45 H.	z - 5000 Hz; 9	9000Ls, 120001	ls, 13500Ls,		
Max RMS Current at Full Power				19.2 A, Low: 38.4 A; Note: C .s, and max voltage for 6000		nodes on 300	OLs and 4500L	s. Current		
Max RMS Current at FSVoltage				e: High 9.6 A, Low: 19.2 A; 4 v 12.8 A; 1 Phase: High: 19.1		High: 4.8, Low	/ 9.6; 1 Phase:	High: 14.4 A,		
Option -EHV Specifications										
Voltage/Frequency Ranges	Voltage: Low:	0-200 Volt; High: 0-400	) Volt / Frequ	uency: With -HF option: 45 H	lz - 5000 Hz					
Max RMS Current at Full Power				15.0 A, Low: 30.0 A; Note: C .s, and max voltage for 6000		nodes on 300	OLs and 4500L	s. Current		
Max RMS Current at FS Voltage		3000Ls: 3 Phase: High: 2.5 A, Low: 5.0 A; 1 Phase: High 7.5 A, Low: 15.0 A; 4500Ls: 3 Phase: High: 3.8, Low 7.5; 1 Phase: High: 11.3 A, Low: 22.5 A; 6000Ls: 3 Phase: High: 5.0 A, Low 10.0 A; 1 Phase: High: 15.0 A, Low: 30.0 A								
Option -HF Specifications	· · · · · · · · · · · · · · · · · · ·									
Measurements:	Parameter	Frequency	Phase	Voltage (AC)	Current (AC rms)	Real Power	Apparent Power	Power Factor		
F < 2000 Hz: See standard Ls Specifications;	Range	45 - 5000 Hz	< 2000 Hz > 2000 Hz	0-300 V < 1000 Hz / > 1000 Hz	0-50 A	0-5 kW	0-5 kVA	0.00-1.00		
F > 2000 Hz: See table >	Accuracy* (±) 1 ø mode (-1)	0.1% + 1 digit	0.5°	0.05% + 250 mV	0.5% + 150 mA	0.5% + 9W	0.5% + 9 VA	0.03		
	3 ø mode (-3)	o,o i i digit	5°		0.5% + 50 mA		0.5% + 3 VA	0.01		
		0.01 Hz / 0.1 Hz / 1 Hz	0.1° / 1°	10 mV	1 mA	1 W	1 VA	0.01		
				e 100 counts. For multi-chassis confi > 50% of max. Frequency measurer				ons are times		
250 mVrms typical (20 kHz to 1 MHz)	3000Ls 34500	Ls, 6000Ls: Standard: -	HV 45 Hz- 5	5000 Hz; - EHV: 45 Hz - 5000	) Hz					
Output Noise										
		250 mVrms typical (20 kHz to 1 MHz)								

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onal, Inc

### Ls Series

Model <sup>1</sup>	Output Power	No of Out	Nom. Input Voltage <sup>2</sup>	
		-1	-3	
3000Ls	3 kVA	1	3	208-230 V
3000Ls-400	3 kVA	1	3	400 V
4500Ls	4.5 kVA	1	3	208-230 V
4500Ls-400	4.5 kVA	1	3	400 V
6000Ls	6 kVA	1	3	208-230 V
9000Ls/2	9 kVA	1	3	208-230 V
9000Ls/2-400	9 kVA	1	3	400 V
12000Ls/2	12 kVA	1	3	208-230 V
13500Ls/3	13.5 kVA	1	3	208-230 V
13500Ls/3-400	13.5 kVA	1	3	400 V
18000Ls/3	18 kVA	1	3	208-230 V

Note 1: The /2 or /3 designation indicates number of chassis.

Note 2: All input voltage specifications are for Line to Line three phase, delta or wye. Model 3000Ls (208 V input) can be operated on 230 V L-N single phase if needed.

HF Table Model	Max. Freq.	Orderir Model
3000Ls	5000 Hz	Refer to t
4500Ls	5000 Hz	configura
6000Ls	5000 Hz	(-1 or -3)
9000Ls/2	2000 Hz	or 4500L
12000Ls/2	2000 Hz	Supplied
13500Ls/3	2000 Hz	User / Pro
18000Ls/3	2000 Hz	Software
		Options Input Op -400
		-480
		Output 0 -AX
		-HV
		-EHV
		-HF
		-LF
		<b>Keypad</b> -KP
		Cabinet ( -RMS
		C prefix
		<b>Controlle</b> -160
		7045

### ng Information

table shown for model numbers and ations. Specify number of output phases as part of model number, eg 4500Ls-1 \_s-3.

### d with

	ogramming Manual on CD-ROM, and RS232C serial cable.
Options Input Op -400	
-480	480 ±10% (3 phase output only)
Output -AX	<b>Options</b> Auxiliary outputs, 26 VAC, 5 VAC. Limits upper frequency to 800 Hz.
-HV	156/312 V output range.
-EHV	200/400 V output range.
-HF	Extends upper frequency limit. See HF table.
-LF	Limits output frequency to 500 Hz.
<b>Keypad</b> -KP	<b>Options</b> Upgraded keypad control panel.



#### Options

-RMS	Rackmount Slides. Recommended for
	rack mount applications.

Cabinet System. Installed and pre-wired in 19" cabinet.

#### er Options

- RTCA/DO-160, Change 2, EuroCAE-14D [Section 16, AC only]
- -704F Mil-Std 704 rev A - F
- -704 Mil-Std 704 rev D and E test firmware. [AC only]

-ABD	Airbus Directive 0100.1.8 tests. [AC only]. Requires -ADV and use of Windows PC and included LxGui software.
-AMD	Airbus AMD24 Test
-A350	Airbus Test Software
-AIRB	Airbus A380, A350 & AMD24 package
-ABL	Emulates Elgar SL Series
-B787	Boeing 787 Test Software
-ADV	Advanced feature set. Adds arbitrary waveform generation and harmonic analysis of voltage and current.
-gpib	GPIB interface and APE programming language.
-LAN	Ethernet Interface.
-MB	Multi-box. Adds controller to auxiliary chassis of multi-chassis systems.
-MODE	Add phase mode selection for 3 models
-L22	Locking Knobs.
-LKM	Clock and Lock Master
-LKS	Clock and Lock Auxiliary
-LNS	Line Sync.
-EXS	External Sync.

#### **Option Matrix**

	HF	LF	нν	ЕНV	LKM	LKS	EXS	АХ
HF	-	х	0	0	х	х	0	х
LF	х	-	0	0	0	0	0	0
нν	0	0	-	х	0	0	0	0
EHV	0	0	х	-	0	0	0	0
LKM	х	0	0	0	-	х	0	0
LKS	х	0	0	0	х	-	х	0
EXS	0	0	0	0	0	х	-	0
AX	х	0	0	0	0	0	0	-

Note 1: See option matrix

Note2 : -LKS, -LNS and -EXS are mutually exclusive and with Ext Trig function.

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